

FIG. 1

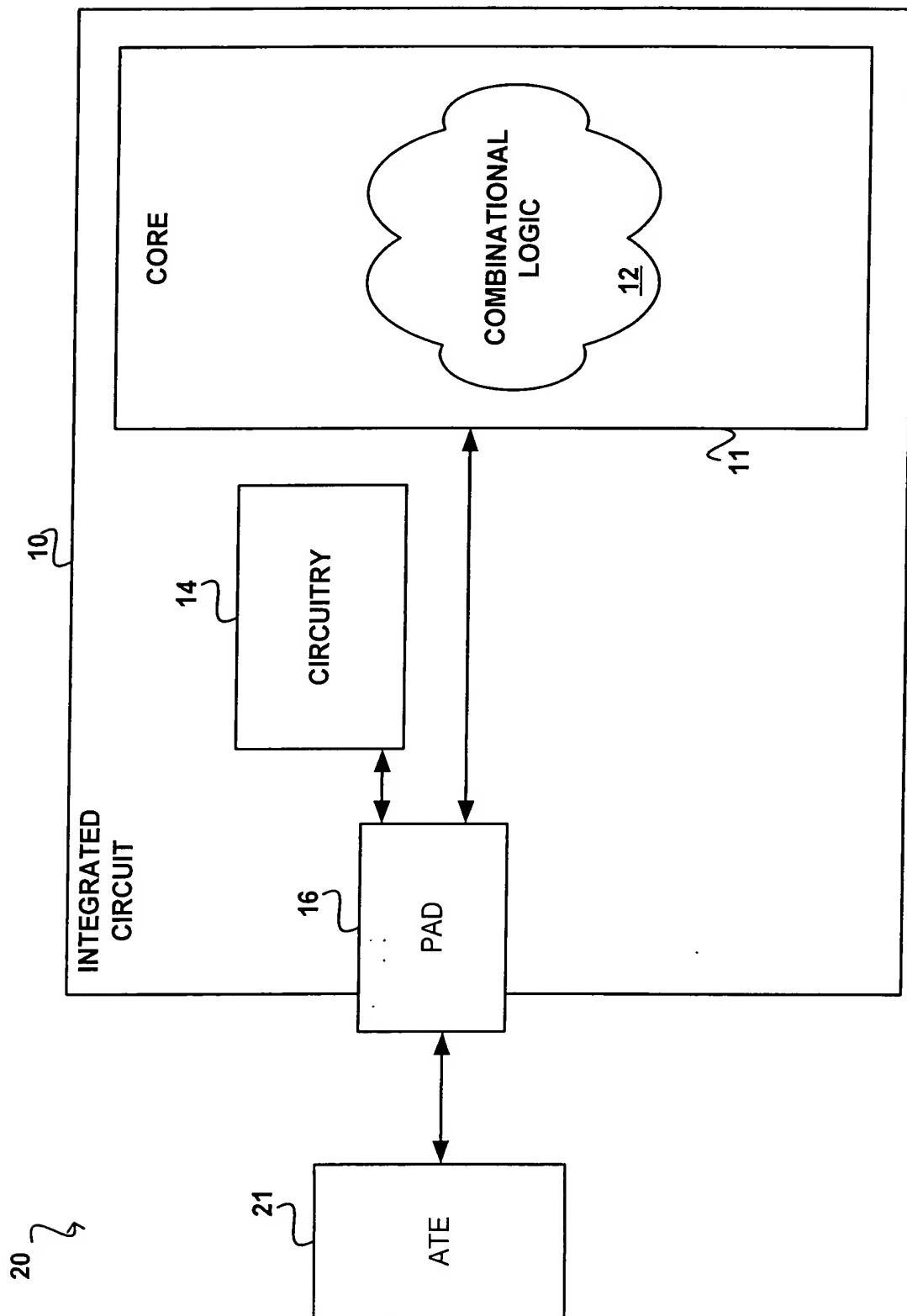


FIG. 2

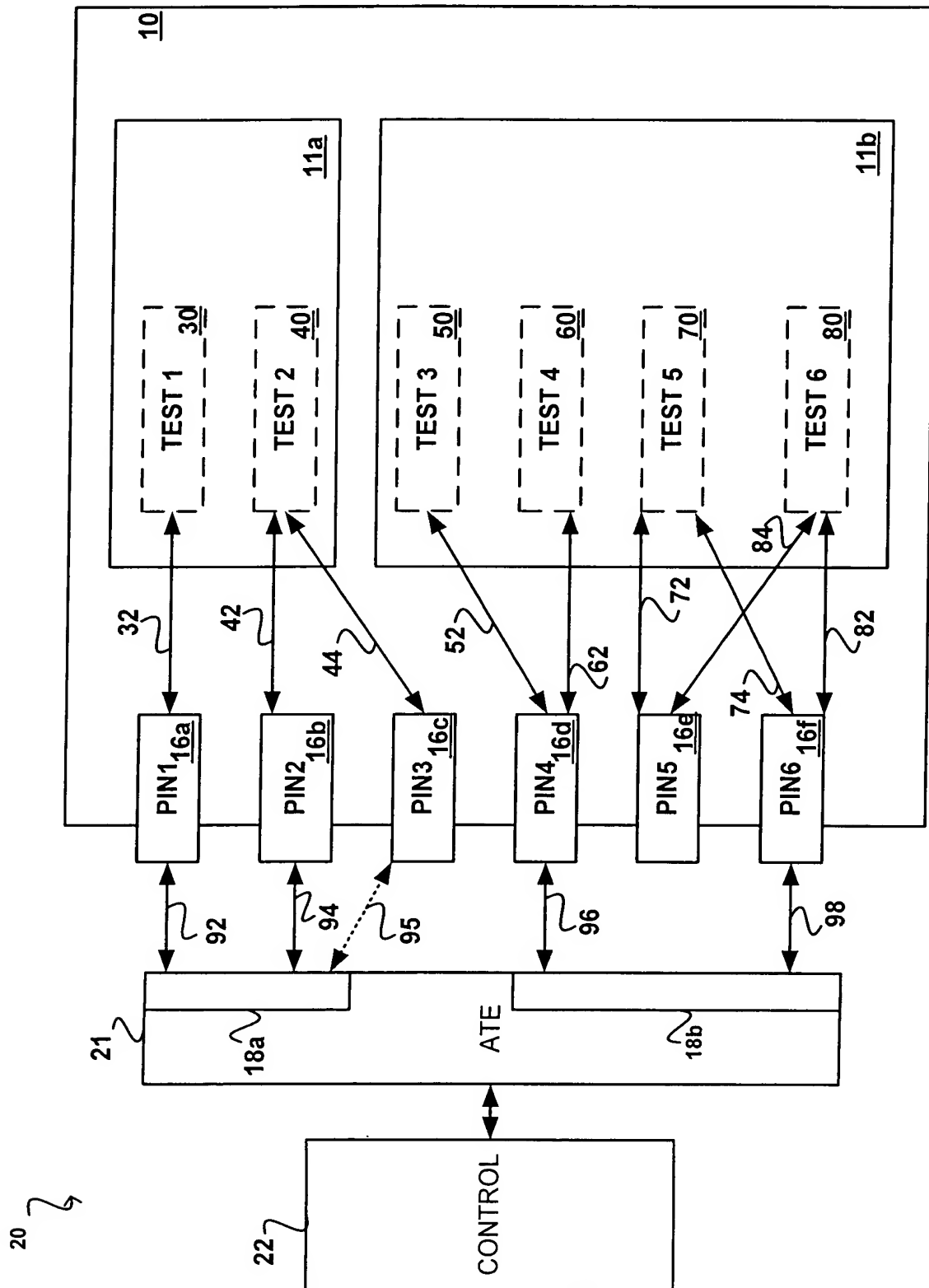


FIG. 3

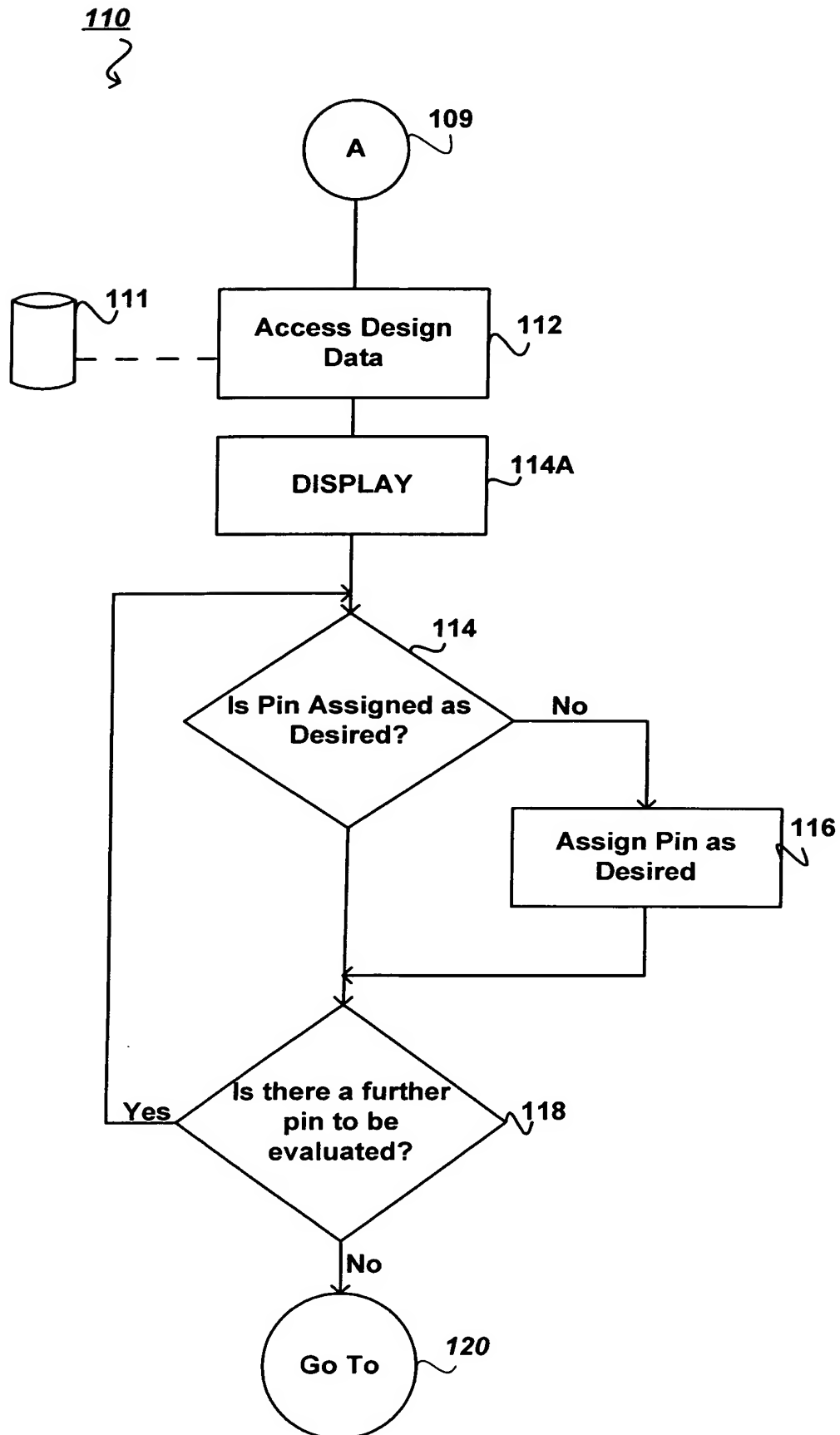


Fig. 4

"Method and System for Graphical Pin Assignment and/or Verification"

Inventor: Robert S. Kolman
Atty. Docket No. 10030574-1

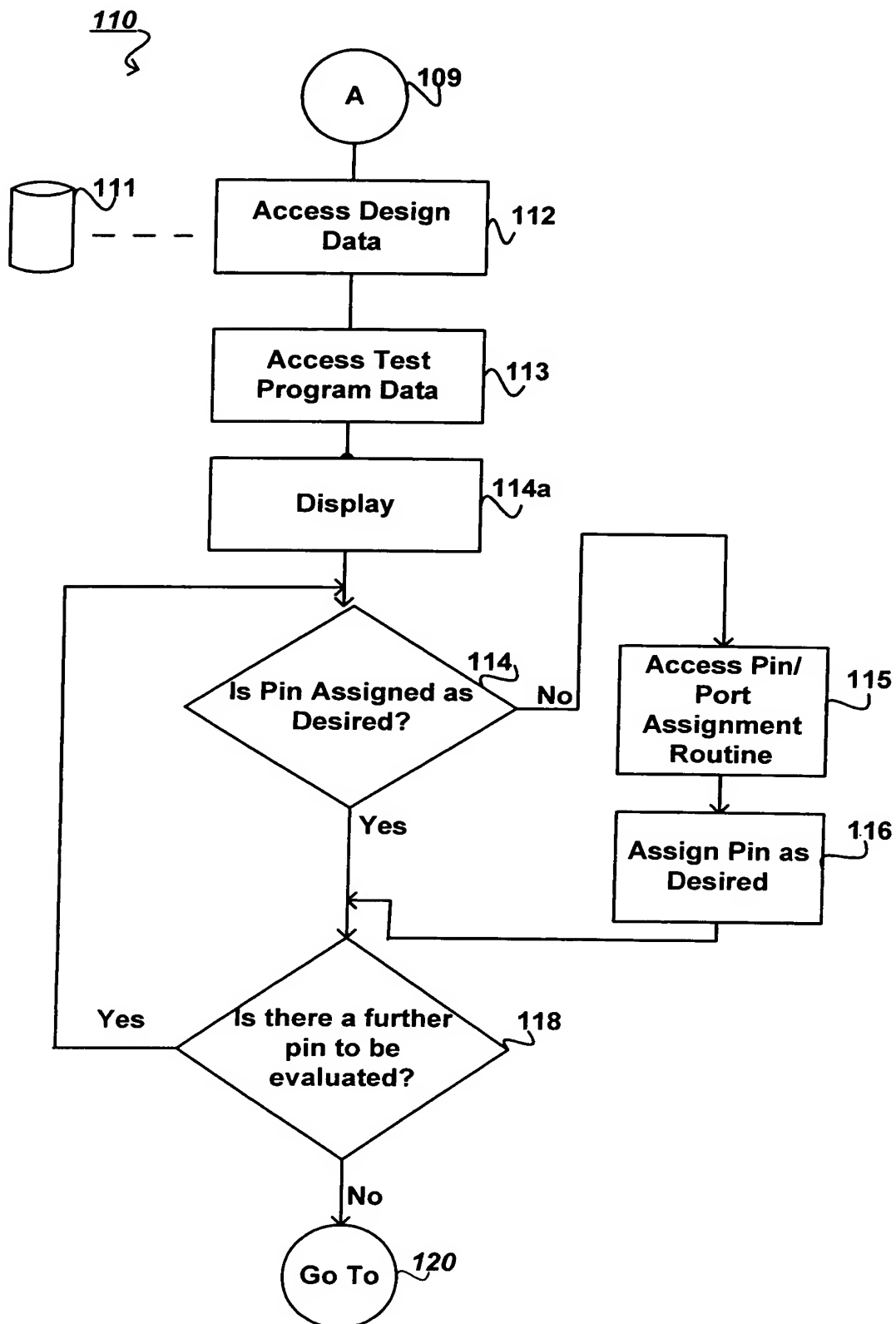


Fig. 5

"Method and System for Graphical Pin Assignment and/or Verification"

Inventor: Robert S. Kolman
Atty. Docket No. 10030574-1

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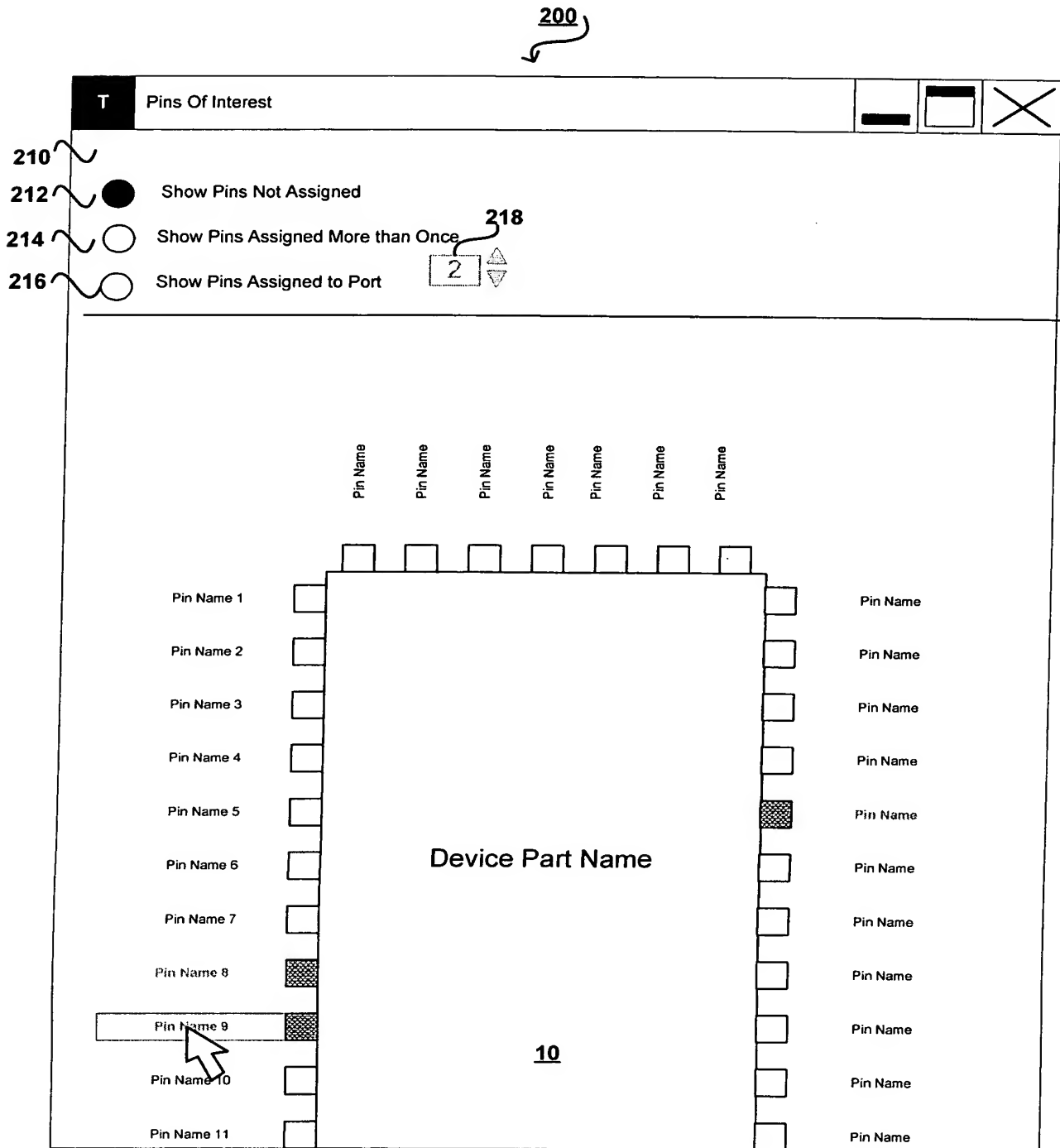


Fig. 6

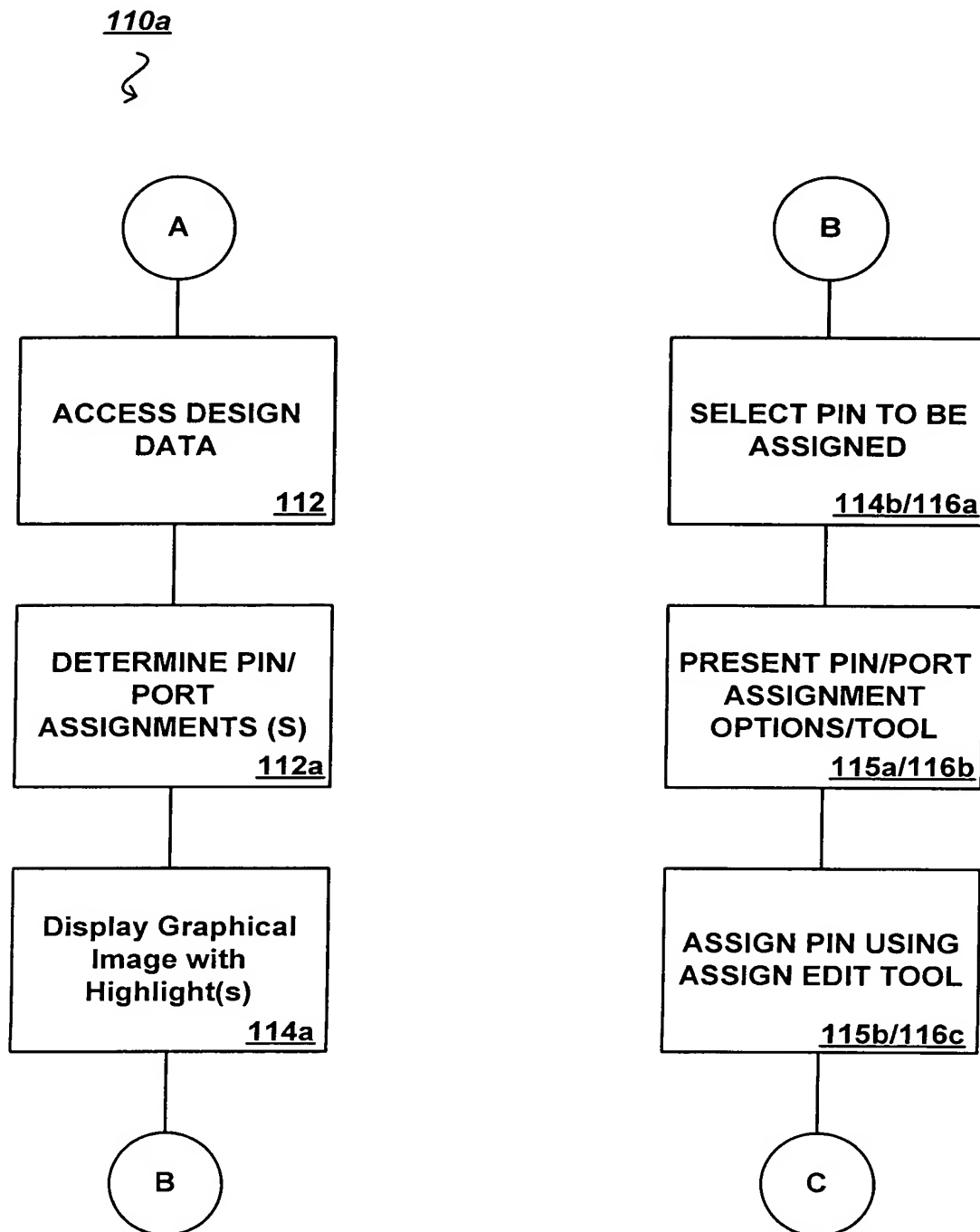


Fig. 7

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↙

PIN PORT ASSIGNMENT

portname: **ADC_PORT**

Filter:

Pinlist:

- Select**
- Select All**

- DAC.CLR
- DAC.DB11
- DAC.DB10
- DAC.DB9
- DAC.DB8
- DAC.DB7
- DAC.DB6
- DAC.DB5
- DAC.DB4
- DAC.DB3
- DAC.DB2
- DAC.DB1
- DAC.DB0
- DAC.ENABLE
- DAC.WDB_TRIG

Port Members:

- ADC.DB7
- ADC.DB6
- ADC.DB5
- ADC.DB4
- ADC.DB3
- ADC.DB2
- ADC.DB1
- ADC.DB0
- ADC.WR
- ADC.WGA_TRIG

Paste

Delete

Save

Cancel

FIG. 8

"Method and System for Graphical Pin Assignment and/or Verification"

Inventor: Robert S. Kolman
Atty. Docket No. 10030574-1

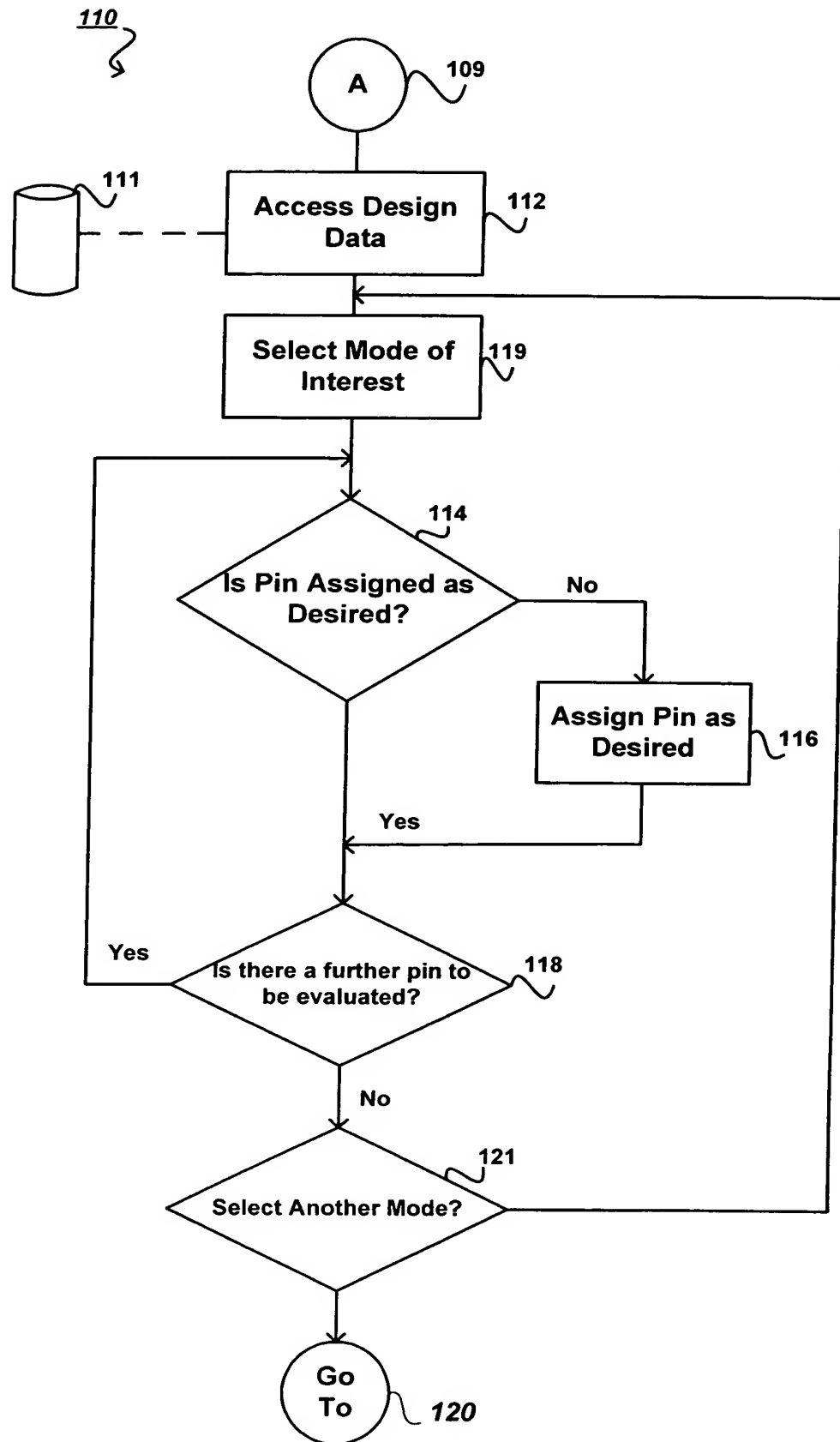


Fig. 9